

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
I1	7169	generat\$3 and ((error signal\$1 or phase error signal\$1) same (phase comparator or phase detector))	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/03/28 09:35
I2	3	receiv\$3 and (error signal\$1 same first clock circuit same second clock circuit)	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/03/28 09:36
I3	1	I1 and I2	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/03/28 09:36
I4	1	generat\$3 same first clock signal same first clock circuit same error signal	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/03/28 10:35
I5	1	first clock circuit same first clock signal same error signal	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/03/28 10:36

L6	1	generat\$3 and ((first clock signal) same (error signal) same (first clock circuit))	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/03/28 10:37
L7	32	generat\$3 same first clock signal same first clock circuit	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/03/28 10:38
L8	21	generat\$3 same second clock signal same second clock circuit	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/03/28 10:38
L9	142720	error signal\$1 or error clock signal\$1	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/03/28 10:38
L10	18	l7 and l8	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/03/28 10:38
L11	3	l9 and l10	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/03/28 10:39

L13	1	I1 and I11	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/03/28 10:39
L14	194	first clock circuit	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/03/28 13:16
L15	251	second clock circuit	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/03/28 13:17
L16	13016	first clock signal	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/03/28 13:17
L17	12634	second clock signal	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/03/28 13:17
L18	142718	error signal or error clock signal	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/03/28 13:17

l19	137	l14 and l15	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/03/28 13:17
l20	61	l19 and l16 and l17	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/03/28 13:17
l21	10	l20 and l18	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/03/28 13:18
l22	2	l21 and l1	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/03/28 13:18
l23	10	l21 and (l1 or l2 or l7 or l8 or l9)	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/03/28 13:18
l24	5067	714/699,700,707,798,799,814;713/400,503,500.ccis.	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/03/28 13:20

25	1	23 and 24	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/03/28 13:20
26	887	clock distribution circuit	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/03/28 13:22
27	3	23 and 26	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/03/28 13:22

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